

Paris, 16-18 October 2018



Organizer:



IMPLEMENTING THE STANDARDISED MAPPING OF TDL TO TTCN-3

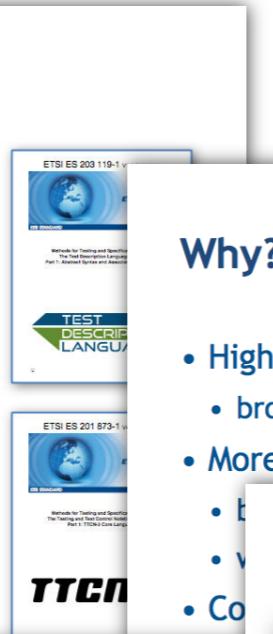
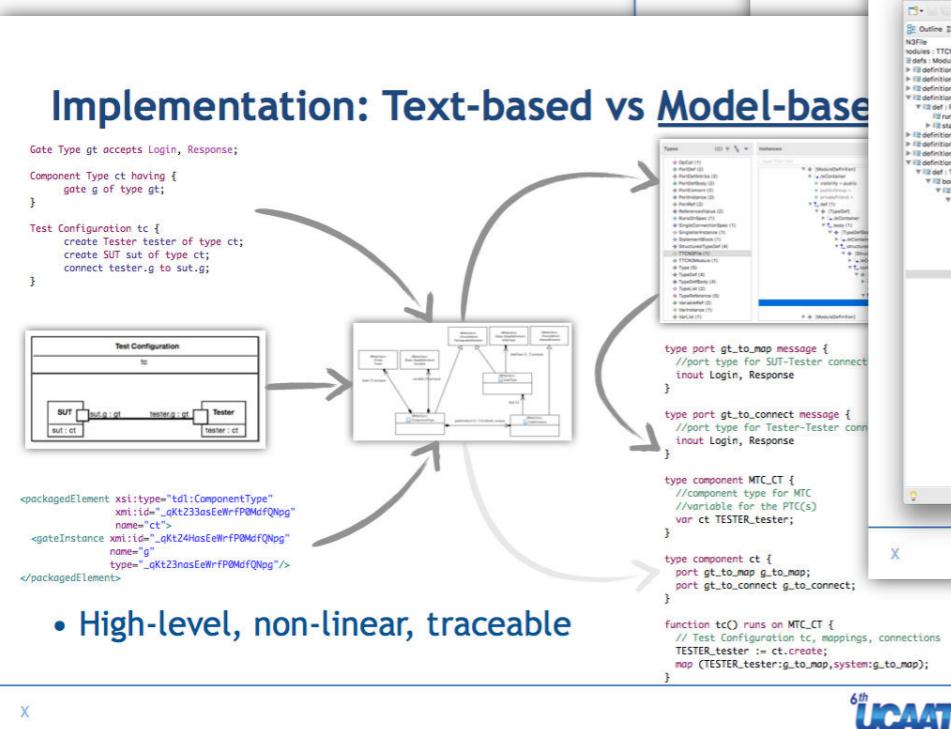
Philip Makedonski (University of Göttingen)

Jens Grabowski (University of Göttingen)

Overview

Background

- Test Description Language
 - Design, documentation, representation of formalised test descriptions
 - Scenario-based approach
 - Testing and Test Control Notation
 - Specification and implementation of all kinds of black-box tests
 - Component-based approach



Why?

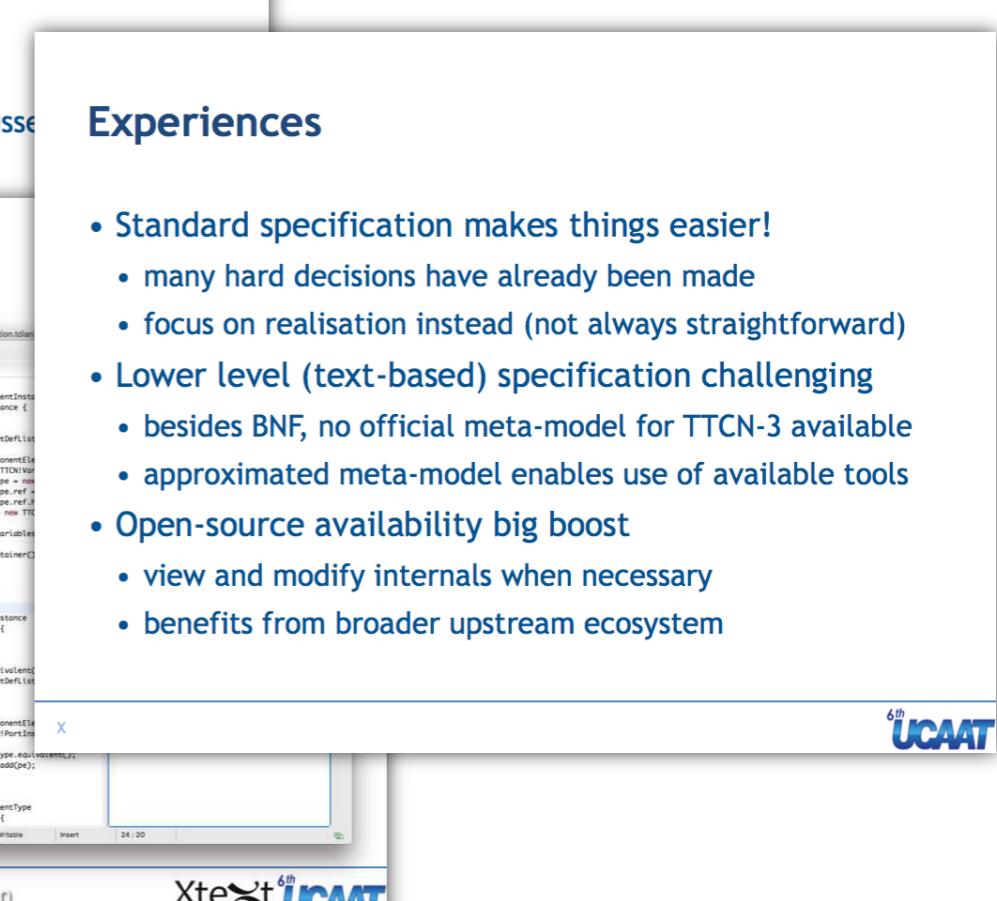
- Highly requested
 - brought up every time the mapping is discussed
 - More comprehensive standard validation
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Tooling

Tooling

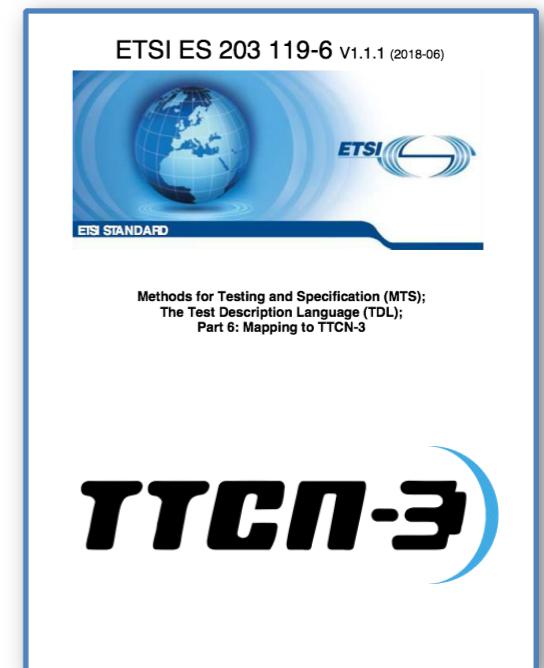
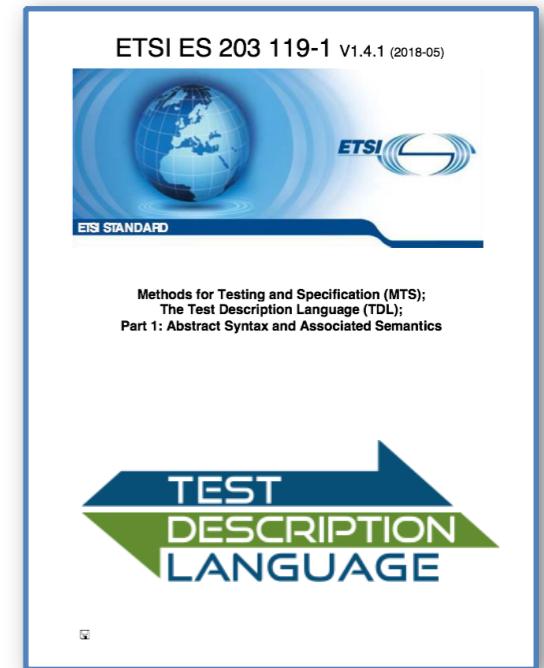
The screenshot shows the Xtext IDE interface with three tabs open:

- Outline**: Shows the Ecore model structure with nodes like `TCM3Module`, `presentation`, `TypeDefinition`, `PortDefinition`, `PortType`, `Port`, `PortRef`, `PortBody`, `PortDefinitionBody`, `PortDefinitionList`, `PortTypeList`, `PortList`, `PortRefList`, `PortBodyList`, and `PortDefinitionBodyList`.
- presentation.tcl3**: The main source file containing the TCM3 presentation logic. It includes imports for `TCM3Module`, `presentation`, `Component`, `Type`, `Port`, `PortRef`, `PortBody`, `PortDefinition`, `PortType`, `PortList`, `PortRefList`, `PortBodyList`, `PortDefinitionBody`, `PortDefinitionList`, `PortTypeList`, and `PortDefinitionBodyList`. The code defines a package `presentation` with a type `Login` and a response `Response`. It also defines a component `Type CT` with a gate `g` and a configuration `Test Configuration ct` that creates a tester of type `ct` and connects it to port `g`.
- tcnMapping.eTL**: A transformation file (eTL) that maps the presentation model to TCM3 oxygen. It uses rule `CT2` to map the `presentation` package to a `TDLComponentDefinition`. Rule `GATE2` maps the `Gate` type to a `TDLGateDefinition`. Rule `TYPE2` maps the `Type` type to a `TDLTypeDefinition`. Rule `PORT2` maps the `Port` type to a `TDLPortDefinition`. Rule `PORTREF2` maps the `PortRef` type to a `TDLPortRefDefinition`. Rule `PORTBODY2` maps the `PortBody` type to a `TDLPortBodyDefinition`. Rule `PORTDEFINITION2` maps the `PortDefinition` type to a `TDLPortDefinition`. Rule `PORTTYPE2` maps the `PortType` type to a `TDLPortTypeDefinition`. Rule `PORTLIST2` maps the `PortList` type to a `TDLPortListDefinition`. Rule `PORTREFLIST2` maps the `PortRefList` type to a `TDLPortRefListDefinition`. Rule `PORTBODYLIST2` maps the `PortBodyList` type to a `TDLPortBodyListDefinition`. Rule `PORTDEFINITIONBODY2` maps the `PortDefinitionBody` type to a `TDLPortDefinitionBodyDefinition`. Rule `PORTDEFINITIONLIST2` maps the `PortDefinitionList` type to a `TDLPortDefinitionListDefinition`. Rule `PORTTYPELIST2` maps the `PortTypeList` type to a `TDLPortTypeListDefinition`. Rule `PORTDEFINITIONBODYLIST2` maps the `PortDefinitionBodyList` type to a `TDLPortDefinitionBodyListDefinition`. Rule `TYPE2` maps the `Type` type to a `TDLTypeDefinition`. Rule `TC2` maps the `TCNModule` to a `TDLComponentDefinition`. Rule `TC2` also maps the `presentation` package to a `TDLComponentDefinition`. Rule `TC2` maps the `Component` type to a `TDLComponentDefinition`. Rule `TC2` maps the `Type` type to a `TDLTypeDefinition`. Rule `TC2` maps the `Port` type to a `TDLPortDefinition`. Rule `TC2` maps the `PortRef` type to a `TDLPortRefDefinition`. Rule `TC2` maps the `PortBody` type to a `TDLPortBodyDefinition`. Rule `TC2` maps the `PortDefinition` type to a `TDLPortDefinition`. Rule `TC2` maps the `PortType` type to a `TDLPortTypeDefinition`. Rule `TC2` maps the `PortList` type to a `TDLPortListDefinition`. Rule `TC2` maps the `PortRefList` type to a `TDLPortRefListDefinition`. Rule `TC2` maps the `PortBodyList` type to a `TDLPortBodyListDefinition`. Rule `TC2` maps the `PortDefinitionBody` type to a `TDLPortDefinitionBodyDefinition`. Rule `TC2` maps the `PortDefinitionList` type to a `TDLPortDefinitionListDefinition`. Rule `TC2` maps the `PortTypeList` type to a `TDLPortTypeListDefinition`. Rule `TC2` maps the `PortDefinitionBodyList` type to a `TDLPortDefinitionBodyListDefinition`.



Background

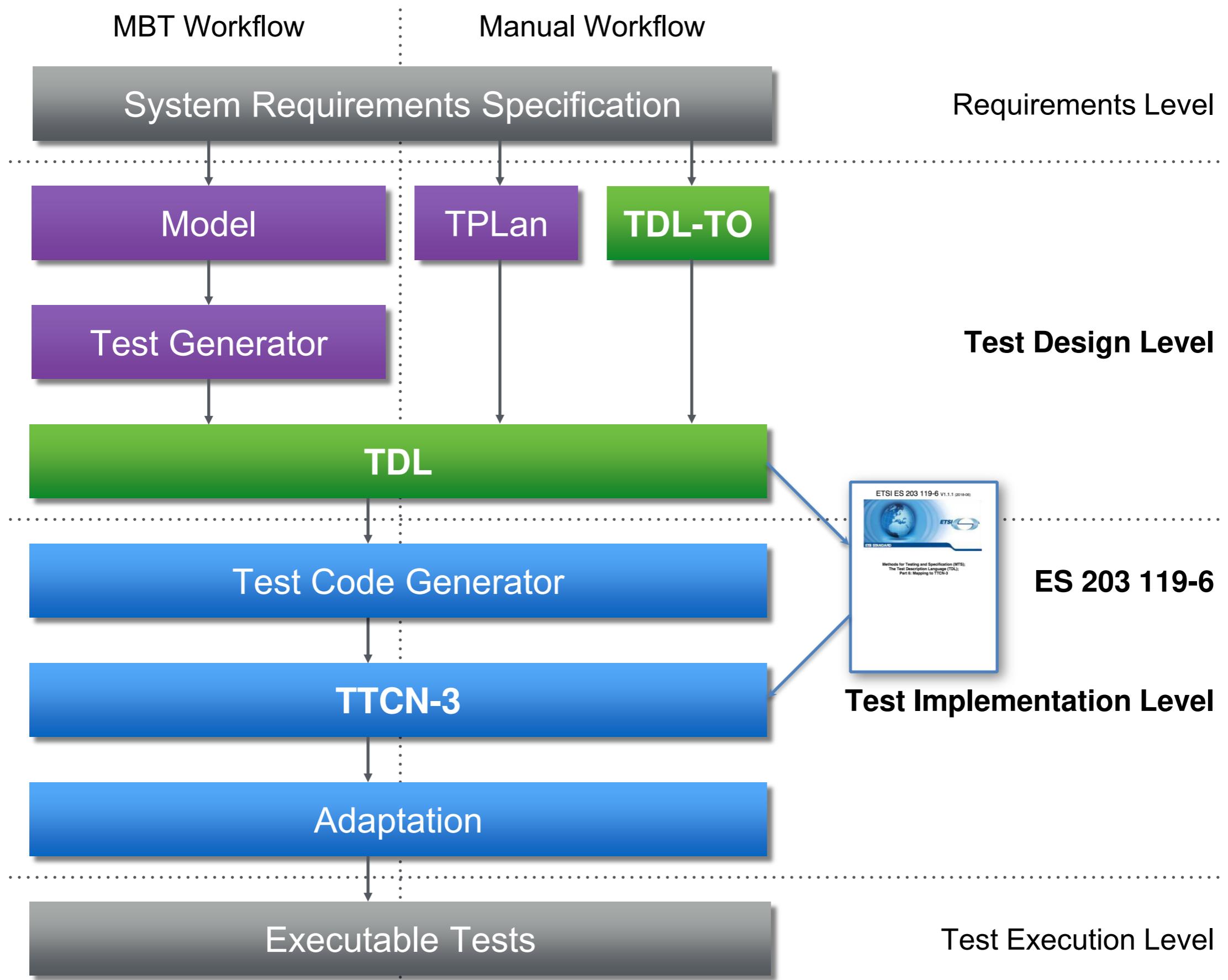
- Test Description Language
 - Design, documentation, representation of formalised test descriptions
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Background

- Establish a connection between TDL and TTCN-3
 - generation of executable tests from test descriptions
 - standardised, ensuring compatibility and consistency
 - re-use existing tools and frameworks for test execution
 - re-use existing TTCN-3 assets (data, behaviour)





Why?

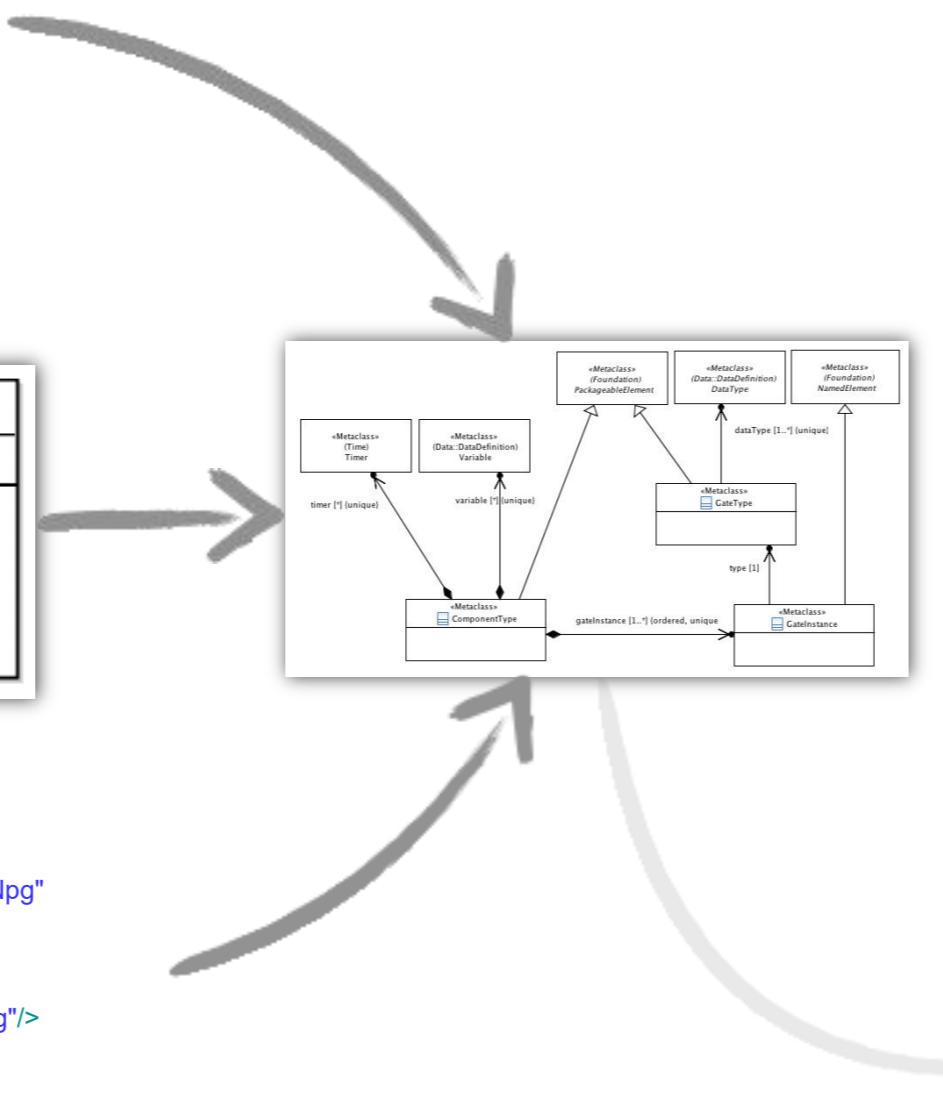
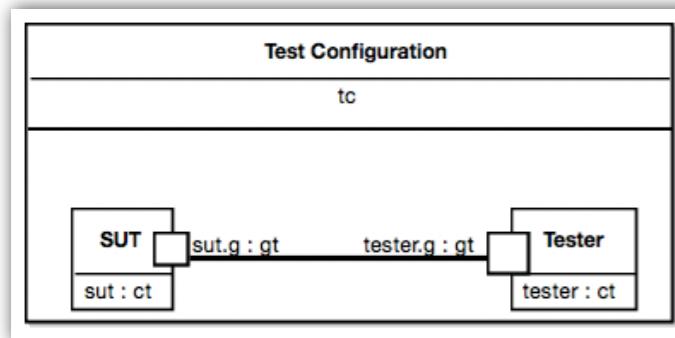
- Highly requested
 - brought up every time the mapping is discussed
- More comprehensive standard validation
 - built on top of initial proof-of-concept prototype
 - wider application of the mapping, address corner cases
- Collect and report on experiences
 - implementation and validation of the standard
 - application of model-based technologies
 - evolving the standard

Implementation: Text-based

Gate Type gt accepts Login, Response;

```
Component Type ct having {
    gate g of type gt;
}
```

```
Test Configuration tc {
    create Tester tester of type ct;
    create SUT sut of type ct;
    connect tester.g to sut.g;
}
```



```

<packagedElement xsi:type="tdl:ComponentType"
  xmi:id="_qKt23nasEeWrfP0MdfQNpg"
  name="ct">
<gateInstance xmi:id="_qKt24HasEeWrfP0MdfQNpg"
  name="g"
  type="_qKt23nasEeWrfP0MdfQNpg"/>
</packagedElement>
  
```

```

type port gt_to_map message {
    //port type for SUT-Tester connections
    inout Login, Response
}

type port gt_to_connect message {
    //port type for Tester-Tester connections
    inout Login, Response
}

type component MTC_CT {
    //component type for MTC
    //variable for the PTC(s)
    var ct TESTER_tester;
}

type component ct {
    port gt_to_map g_to_map;
    port gt_to_connect g_to_connect;
}

function tc() runs on MTC_CT {
    // Test Configuration tc, mappings, connections
    TESTER_tester := ct.create;
    map (TESTER_tester:g_to_map,system:g_to_map);
}
  
```

- Linear, complex, limited, messy

Implementation: Text-based vs Model-based

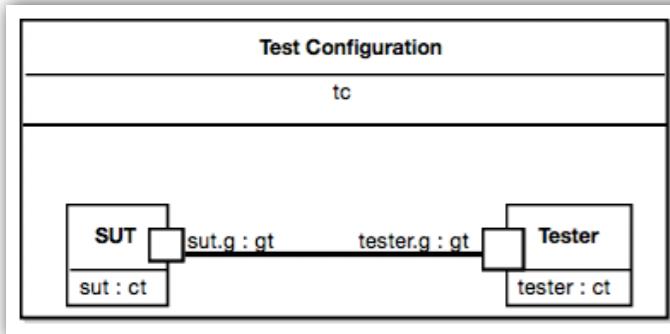
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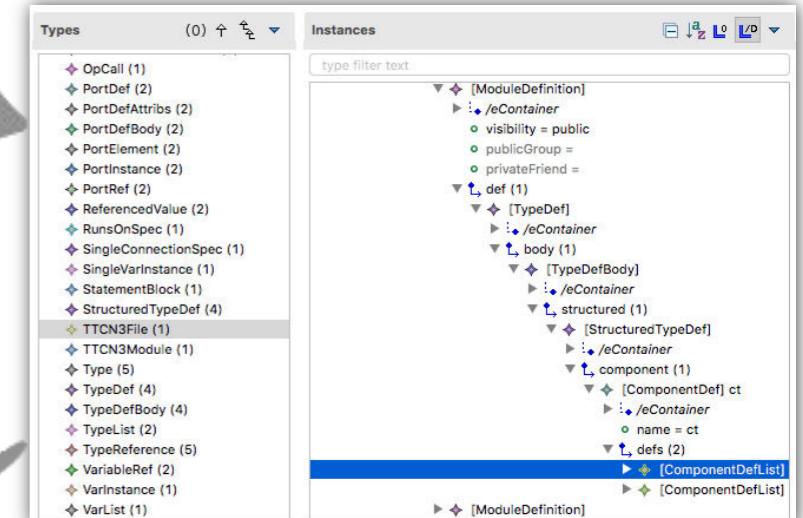
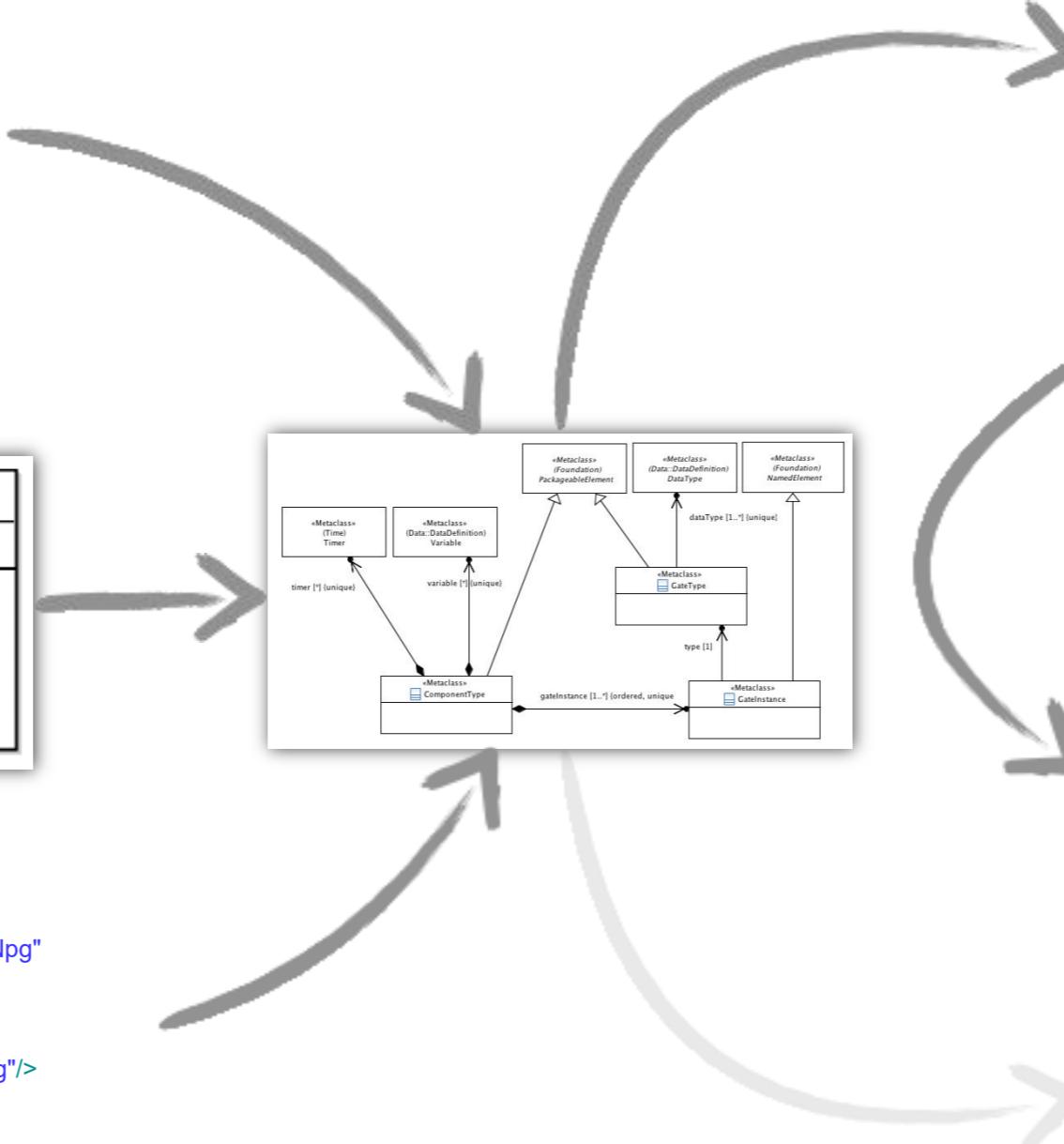
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}

```

- High-level, non-linear, traceable

Model-based Mapping

- Work with higher level structural representation
 - target structure rather complex
 - syntactical details derived automatically
 - non-linear approach for stepwise enrichment
 - traceability and references to equivalent constructs
 - structural validation already during transformation
- But:
 - standard described with text-based mapping
 - no official meta-model for TTCN-3

Tooling

- Eclipse + EMF - modelling platform
- Xtext - textual mapping for models
- Epsilon / ETL - model-to-model transformation
- MoDisco - tree-based model editing (optional)
- Sirius - graphical model editing (optional)
- TOP - EMF-based TDL tools
- T Rex v2 / t3tools v2 - EMF-based TTCN-3 tools
- Custom automation tools



Tooling

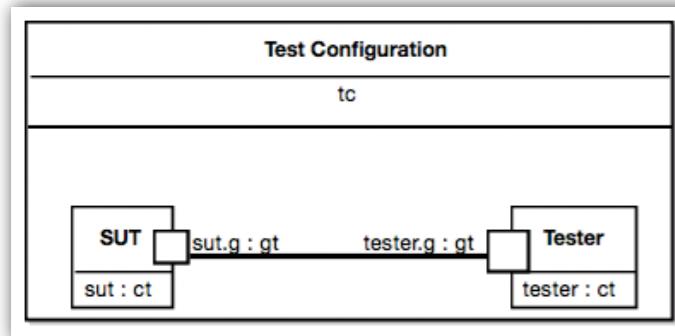
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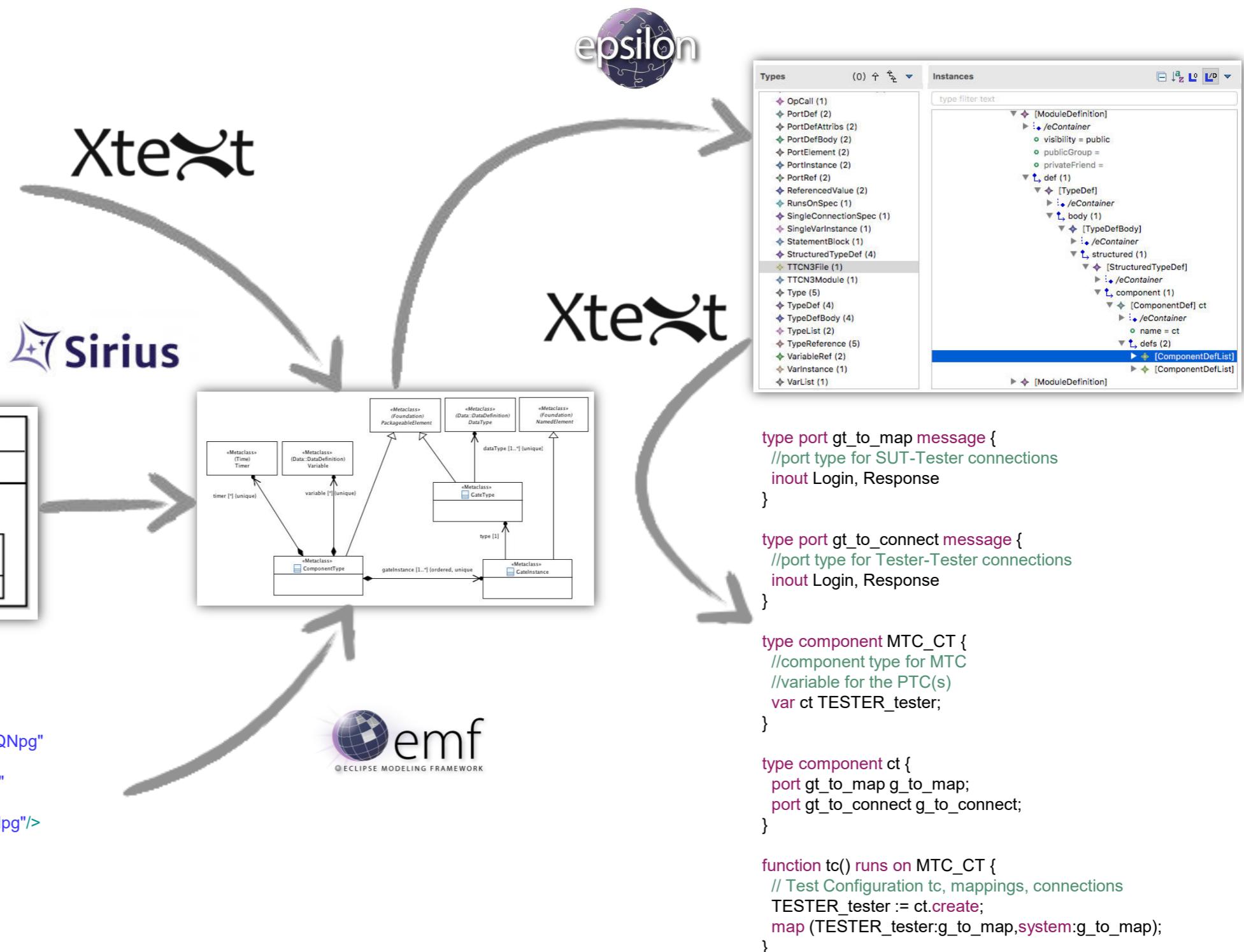
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  name="g"
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</packagedElement>

```



Tooling

The screenshot displays the Eclipse IDE interface with three open editors:

- presentation.tdlan2**: Shows the original TDL (Text Domain Specific Language) code. It includes definitions for a package named "presentation", types "Login" and "Response", and a gate type "gt" that accepts "Login" and "Response". It also defines a component type "ct" with a gate "g" of type "gt", and a test configuration "tc" that creates a tester and connects it to a SUT.
- ttcn3mapping.etl**: Shows the Xtext code for generating TTCN-3 mappings. It contains rules for transforming TDL components into TTCN-3 structures. Key parts include:
 - A rule "CI2V" that transforms a TDL component instance into a TTCN-3 variable. It sets the variable's name to the component's name and creates a component definition list "d" with an empty string "sc".
 - A rule "GIZPE" that transforms a TDL gate instance into a TTCN-3 port element. It sets the port element's name to the gate's name and creates a component definition list "c" with an empty string "sc".
 - A rule "CT2C" that transforms a TDL component type into a TTCN-3 component definition.
- presentation.tdlan2.ttcn3m.ttcn3**: Shows the generated TTCN-3 code. It includes:
 - A module "presentation" containing a type "charstring" and functions for creating "tester" and "sut" instances.
 - A type "component MTC_tc" with variables "ct tester" and "ct sut".
 - A type "component SYSTEM_tc".
 - A type "component ct" with a port "gt g".
 - A type "port gt message" with inout ports "Login" and "Response".

Tooling

Eclipse IDE - tdl-to-ttcn3-oxygen - org.etsi.mts.tdl.ttcn3.examples/models/presentation/presentation.tdlan2.ttcn3m

```

rule CI2V
    transform ci : TDL!ComponentInstance
    to v : TTCN!SingleVarInstance {
        v.name = ci.TTCNname();

        var d = new TTCN!ComponentDefList();
        d.sc = ";";
        d.element = new TTCN!ComponentElementDef();
        d.element.variable = new TTCN!VarInstance();
        d.element.variable.listType = new TTCN!TypeC
        d.element.variable.listType.ref = new TTCN!T
        d.element.variable.listType.ref.head = ci.type
        d.element.variable.list = new TTCN!VarList()
        d.element.variable.list.variables.add(v);

        var mtc = ("MTC_" + ci.eContainer().TTCNname())
        mtc.defs.add(d);
    }

rule GI2PE
    transform gi : TDL!GateInstance
    to pe : TTCN!PortElement {
        pe.name = gi.TTCNname();

        var c = gi.eContainer.equivalent();
        var d = new TTCN!ComponentDefList();
        c.defs.add(d);

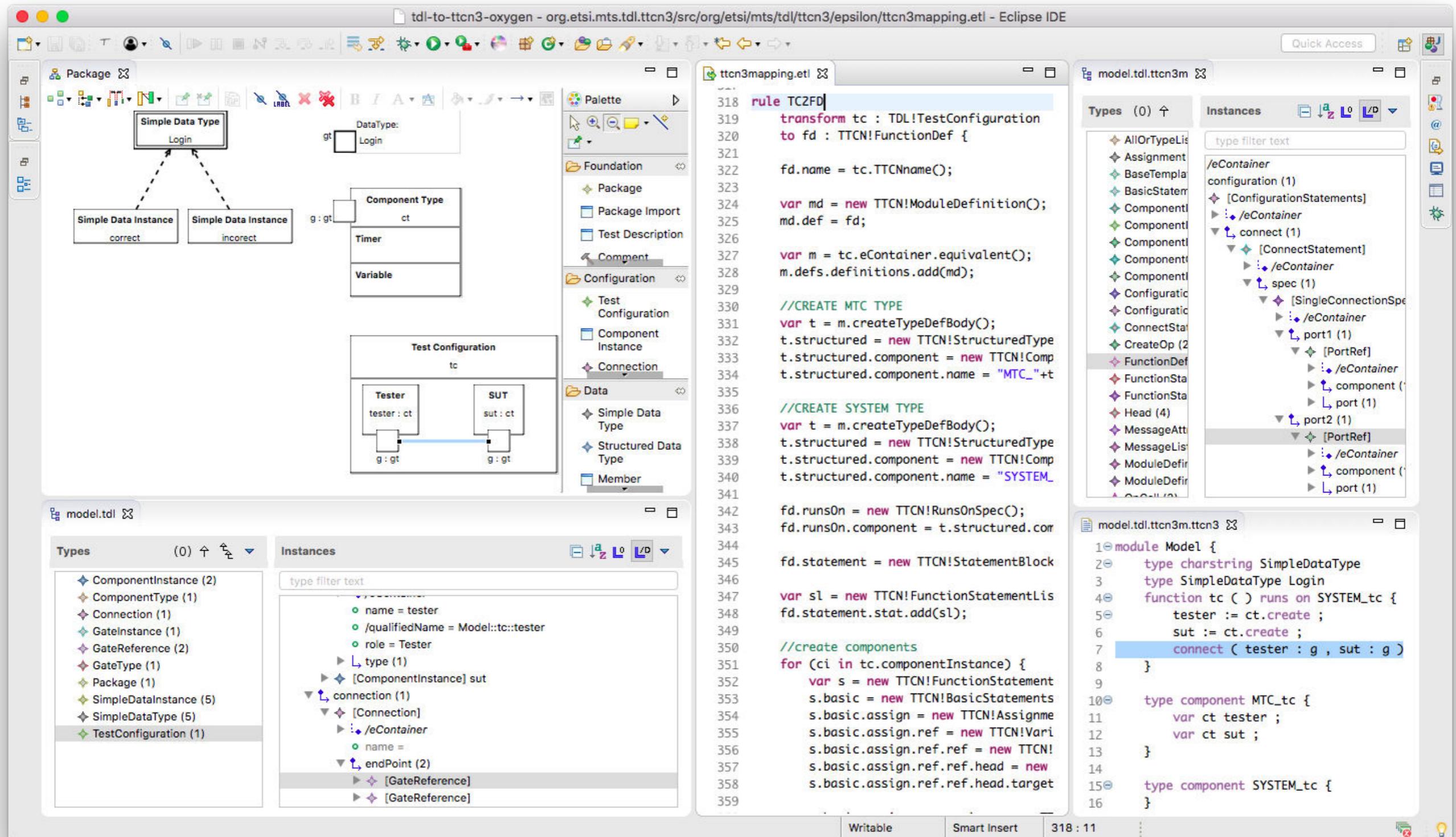
        d.sc = ";";
        d.element = new TTCN!ComponentElementDef();
        d.element.port = new TTCN!PortInstance();
        d.element.port.ref = gi.type.equivalent();
        d.element.port.instances.add(pe);
    }

rule CT2C
    transform ct : TDL!ComponentType
    to c : TTCN!ComponentDef {
}

```

Selected Object: configuration (1)

Tooling



Experiences

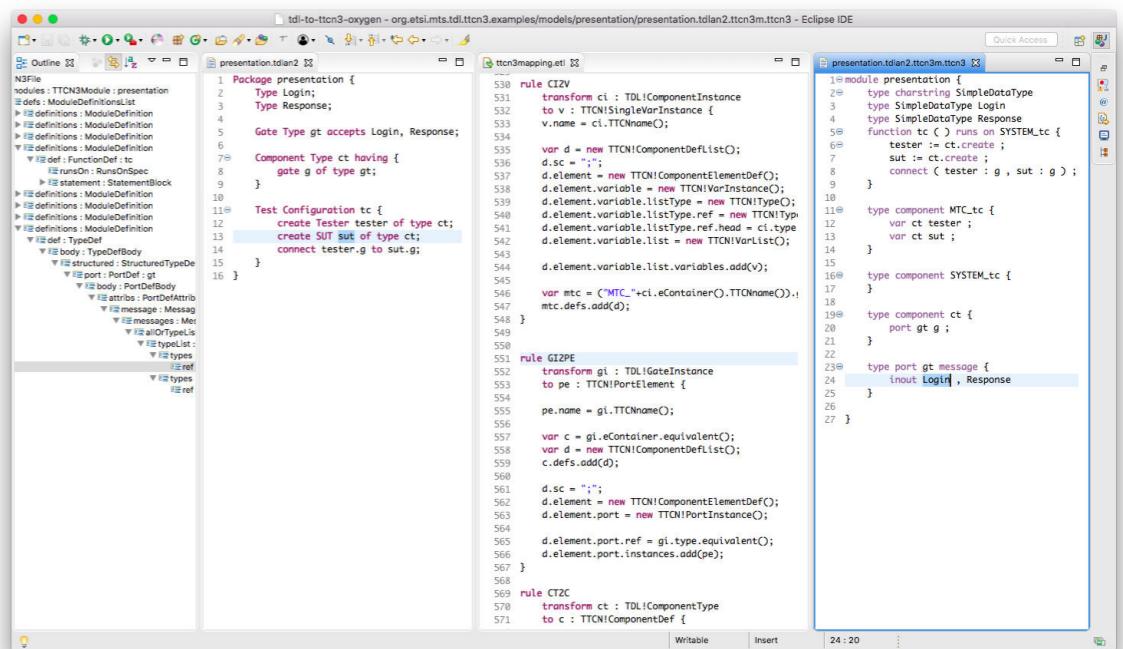
- Standard specification makes things easier!
 - many hard decisions have already been made
 - focus on realisation instead (not always straightforward)
- Lower level (text-based) specification challenging
 - besides BNF, no official meta-model for TTCN-3 available
 - approximated meta-model enables use of available tools
- Open-source availability big boost
 - view and modify internals when necessary
 - benefits from broader upstream ecosystem

Experiences

- Model-based approach
 - focus on essential parts - transformation logic
 - building blocks provided by the platform
 - convenient stepwise enrichment
- Custom tooling to streamline repetitive tasks
 - translation between different formats
 - expected TTCN-3 to model (for analysis)
 - TDlan to model
 - model to model transformations
 - model to TTCN-3

Conclusion

- Current status
 - ~80% of specification covered
 - still a prototype
 - open for contributions
- Future work
 - further refinement towards 100% specification coverage
 - comprehensive testing and evaluation
 - TTCN-3 to TDL?



The screenshot shows the Eclipse IDE interface with several open files:

- `presentation.tdian2`: An N3File containing definitions for modules like `TTCN3Module`, `presentation`, and `Test Configuration tc`.
- `ttcn3mapping.etl`: A script file with ETL code for rules like `CIZV` and `GZIP`, which transform between TDL and TTCN-3 types.
- `presentation.tdian2.ttcn3m.ttcn3`: Another script file containing TTCN-3 code for components like `MTC_tc` and `SYSTEM_tc`.